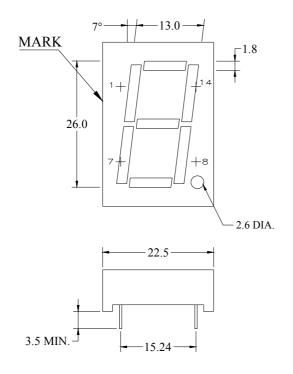
# **SPECIFICATION**

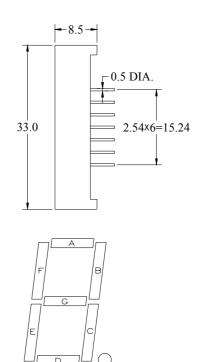
PART NO.: LA(C)2621-21 EWRN
1.02"(26.0mm) SINGLE DIGIT DISPLAY



| Approved by | Checked by | Prepared by |
|-------------|------------|-------------|
| Sam         | Jimmy      | Danny       |

#### **Dimensions**



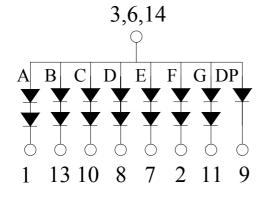


#### Notes:

- 1. THE SLPE ANGLE OF ANY PIN MAY BE  $\pm 5.0^{\circ}$  MAX.
- 2. ALL DIMENSIONS ARE IN mm, TOLERANCE IS  $\pm 0.25$ mm UNLESS OTHERWISE NOTED.

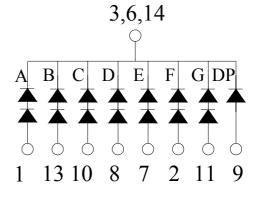
#### **Internal Circuit Diagram**

LA2621-21



NO PIN: 4,5,12

LC2621-21



NO PIN: 4,5,12

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## LA(C)2621-21

## **Description**

| Part No.       | LED Chip |                | Face Color |          |  |
|----------------|----------|----------------|------------|----------|--|
|                | Material | Emitting Color | Surface    | Segments |  |
| LA2621-21 EWRN | GaP/GaP  | Green          | Grey       | White    |  |
| LC2621-21 EWRN | GaP/GaP  | Green          | Grey       | White    |  |

## **Absolute Maximum Ratings at Ta=25 ℃**

| Parameter  | Symbol        |     | nbol Rating     |                         |  |
|--|---------------|-----|-----------------|-------------------------|--|
| Dawar Dissinction Per Comment                            | PD            | DP  | 78              | mW                      |  |
| Power Dissipation Per Segment                            |               | SEG | 156             |                         |  |
| Pulse Current(1/10Duty Cycle,0.1ms Pulse Width.)Per Chip | IFP           |     | 100             | mA                      |  |
| Forward Current Per Chip                                 | IF            |     | 30              | mA                      |  |
| Reverse (Leakage)Current Per Chip                        | Ir            |     | 100             | uA                      |  |
| Reverse Voltage Per Chip                                 |               | Vr  | 5               | V                       |  |
| Operating Temperature Range                              | Topr.         |     | Copr25 to +85   |                         |  |
| Storage Temperature Range                                | Tstg40 to +10 |     | -40 to +100     | $^{\circ}$              |  |
| Lead Soldering Temperature.(1.6mm from seating plane)    | Tsol.         |     | 260 for 5s MAX. | $^{\circ}\! \mathbb{C}$ |  |

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## **Electrical and Optical Characteristics:**

| Parameter   | Symbol |     | Condition    | Min. | Тур. | Max. | Unit |
|---|--------|-----|--------------|------|------|------|------|
| Luminous Intensity Per Segment                      | Iv     |     | If=10mA/seg. | 1.8  | 4.6  |      | mcd  |
| Forward Voltage                                     | Vf     | DP  | If=20mA/seg. |      | 2.1  | 2.6  | V    |
|   |        | SEG | If=20mA/seg. |      | 4.2  | 5.2  | V    |
| Peak Wavelength                                     | λΡ     |     | If=20mA/seg. |      | 567  |      | nm   |
| Dominant Wavelength                                 | λ      | D   | If=20mA/seg. |      | 572  |      | nm   |
| Reverse Current Per Chip (Leakage Current Per Chip) | ]      | [r  | Vr=5V        |      |      | 100  | μΑ   |
| Spectrum Line Halfwidth                             | Δλ     |     | If=20mA/seg. |      | 30   |      | nm   |
| Response Time                                       |        | Γ   |              |      | 250  |      | ns   |

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#### LA(C)2621-21

# Typical Electrical/Optical Characteristic Curves (25°C Ambient Temperature Unless Otherwise Noted)

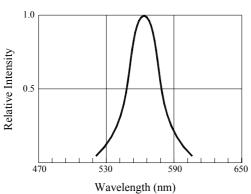


Fig.1 RELATIVE INTENSITY VS. WAVELENGTH

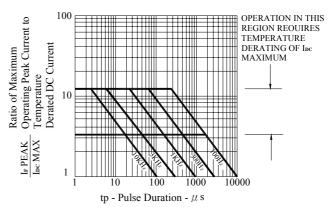


Fig.2 MAXIMUM TOLERABLE PEAK CURRENT VS. PULSE DURATION

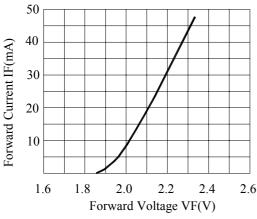
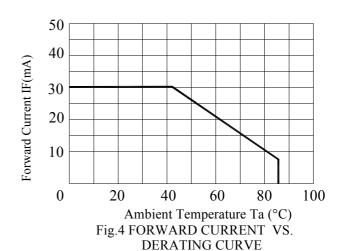


Fig .3 FORWARD CURRENT VS. FORWARD VOLTAGE PER CHIP



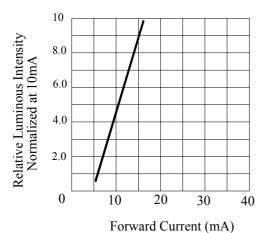
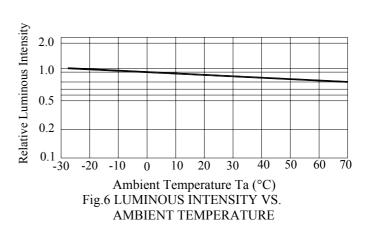


Fig.5 RELATIVE LUMINOUS INTENSITY VS. FORWARD CURRENT



#### **Precautions in Use:**

## PLEASE PAY SPECIAL ATTNTION TO THE NEXT POINT TO INCORPORATE OPTO DEVICE TO HIGH RELIABILITY

1. Do not bend the lead. Bending leads could cause breakage of leads or the degradation of the chip.

When bending is unavoidable, strictly follow the cautionary instruction below.

- (1)Bend the leads before soldering.
- (2)Bending a lead must be done by fixing a lead tightly and applying no stress on the resin part.
- (3) The lead bending point must be more than 1.6mm away from the edge or the resin part.
- (4) When a pin is tested for its endurance, bending degree should be 45° and repeated no more than two times.
- 2. Setting a product by using tool such as a holder should be avoided.

When necessary, no stress should be applied to the resin part and lead to consider dimension tolerance, thermal expansion, thermal contraction of holder, product and circuit board etc.

- 3. The hole pitch of a circuit board must fit into the lead pitch of products.
- 4. When soldering, care the followings:
  - (1)Do not heat a product under any stress (i.e.: twist) to leads.
  - (2)Do not heat ( for example, by soldering ) a product while out side force is applied the resin part.
  - (3) The temperature of a product should not exceed the specified maximum storage temperature.
  - (4) Soldering with PC Board should be conducted with following conditions.
    - (a) For dip soldering

Pre-heating: 90°C Max. for within 60 Sec.

Soldering bath :  $260\pm5^{\circ}$ C (Solder Temp.) for within 5 Sec.

- (b) Soldering iron : 350°C (Soldering iron tip) for within 3 Sec.
- 5. Flux could corrode the leads. Use flux that contains as little chlorine as possible (RA, RMA, less than 0.2 wt%) and need not be washed way. When, however, washing is necessary, partially wash around the leads, instead of the entire LED, by the following conditions.

Cleaning agent: Methyl Alcohol

Cleaning temp : 45°C MAX. Cleaning time : 30Sec. MAX.

- 6. Minimum amount of soldering flux should be used. Soldering flux should be applied only to the pin portion.
- 7. The following may damage products or LED chips: Attachment or contact of residual flux solvent onto the product surface or to LED chips, or invasion of the same into the product.

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